MULTICORE WORLD 2014

25th - 26th February 2014, Auckland, New Zealand

Answering the BIG QUESTIONS

Big Data - Big Processing Power - Big Storage - Big Power Efficiencies - Big Security Solutions

All modern computers are now multicore and increasingly so; we need the programming and application expertise to harness this current transformation. **This is a permanent change that affects all computing**, presenting unique opportunities for New Zealand. *"As we enter the parallel processing era, learning to interact with multicore technology is a critical priority"* - Forbes Magazine (Dec. 2012)

Workshops collocated with Multicore World 2014: details at **www.MulticoreWorld.com**
COMPUTING for SKA, 27th–28th February **/ Introduction to ERLANG**, 27th February

III Multicore World will be at **Auckland University of Technology's Sir Paul Reeves Building**. Sessions cover latest developments in software and hardware, applications and use cases & business opportunities behind these trends.

Early bird tickets available till February 3, 2014
The III Multicore World conference and workshop consist of full days of intensive talks, panels and plentiful discussion time.

Tuesday 25th   Multicore World Day 1, 8:30 - 17:30 / Conference Dinner 18:30 - 22:00
Wednesday 26th   Multicore World Day 2, 9:00 - 17:30 / Closure cocktail 18:00 - 20:30
Thursday 27th   ASKAIC board meeting / Computing for SKA Day 1, 10:00 - 17:00 / Workshop Dinner 18:00 - 22:00
Friday 28th   Computing for SKA Day 2, 9:00 - 17:00 / Closure 17:30 - 20:00

MULTICORE WORLD 2014 - Selected speakers

John Gustafson - Fmr Senior Fellow, Chief Product Architect, AMD (USA). *"A New Kind of Parallelism: The Ubox Method"*.

Gernot Heiser - Leader of SSRG at NICTA (Australia). *"The Revolution is On - Dependable Software is Becoming Affordable"*

Tim Cornwell - SKA Architect - SKA Organisation (UK). *"Construction of SKA Computing"*

Kent Winchell - Office of the CTO, Cray, Inc. (USA). *"Scaling HPC applications in a core rich environment"*

Manuel Chakravarty - A/Professor, University of New South Wales (Australia). *"Embedded Languages for HPC in Haskell"*

Wolfgang Schroder-Preikschat - Professor, FAU (Germany). *"Embedded Computing Systems in the Multicore Era"*

Shaun Hendy - FRSNZ - Professor of Physics, The University of Auckland (New Zealand). *"Get off the grass with Multicore"*

Andreas Wicenec - Professor, University of Western Australia. *"Comparative Scalability I/O Studies in HPC Clusters"*

David Brebner - Founder, CEO, Unlimited Realities (New Zealand). *"Exploiting the new Kaveri CPU/GPU architecture"*

Mark Moir - Principal Investigator, Oracle (NZ). *"Adaptive Integration of Hardware and Software Lock Elision Techniques"*

Ricardo Rocha - Cloud Engineer, Catalyst IT (New Zealand). *"Scalable distributed storage with CEPH"*

Pavlo Baron - Lead Technologist, codecentric AG (Germany). *"Data on its way to history, interrupted by analytics and silicon"*

John Bancroft - Head Asia Pacific, STFC (UK - NZ). *"Big Data, e-infrastructure and the Economic Impact Opportunity"*

Nicola Gaston - President, New Zealand Association of Scientists. *"The problem of scale in computational nanoscience"*

Jasper Horrell - General Manager, Science Computing and Innovation, SKA (South Africa).

Brent Carlson - Lead System Engineer, NRC (Canada). *"PowerMX: a flexible platform specification for signal processing"*

John Humphreys - Chair, ASKAIC (Australia). *"SKA - Scientific Nirvana or Data-led Revolution?"*

Cristina Cifuentes - Founding Director, Oracle Labs Australia. *"Program Transformation: Code Generation to a Massively Parallel Machine and Scalable Analysis using Multicore"*

CONTACT Conference Director, **Nicolas Erdody**, nicolas@multicoreworld.com (+64) 27 5214020. Visit **MulticoreWorld.com,** follow **@MulticoreWorld**